

Fig. 1

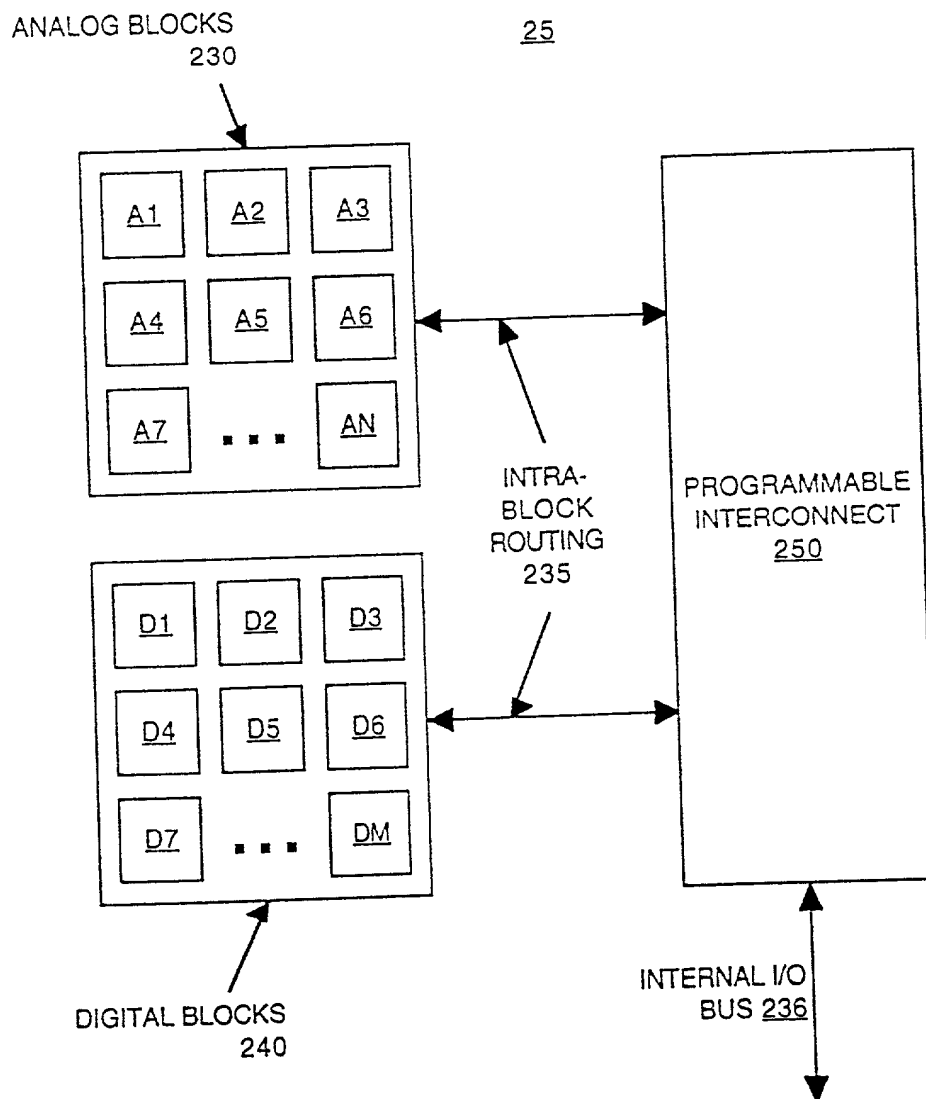


Figure 2A

270

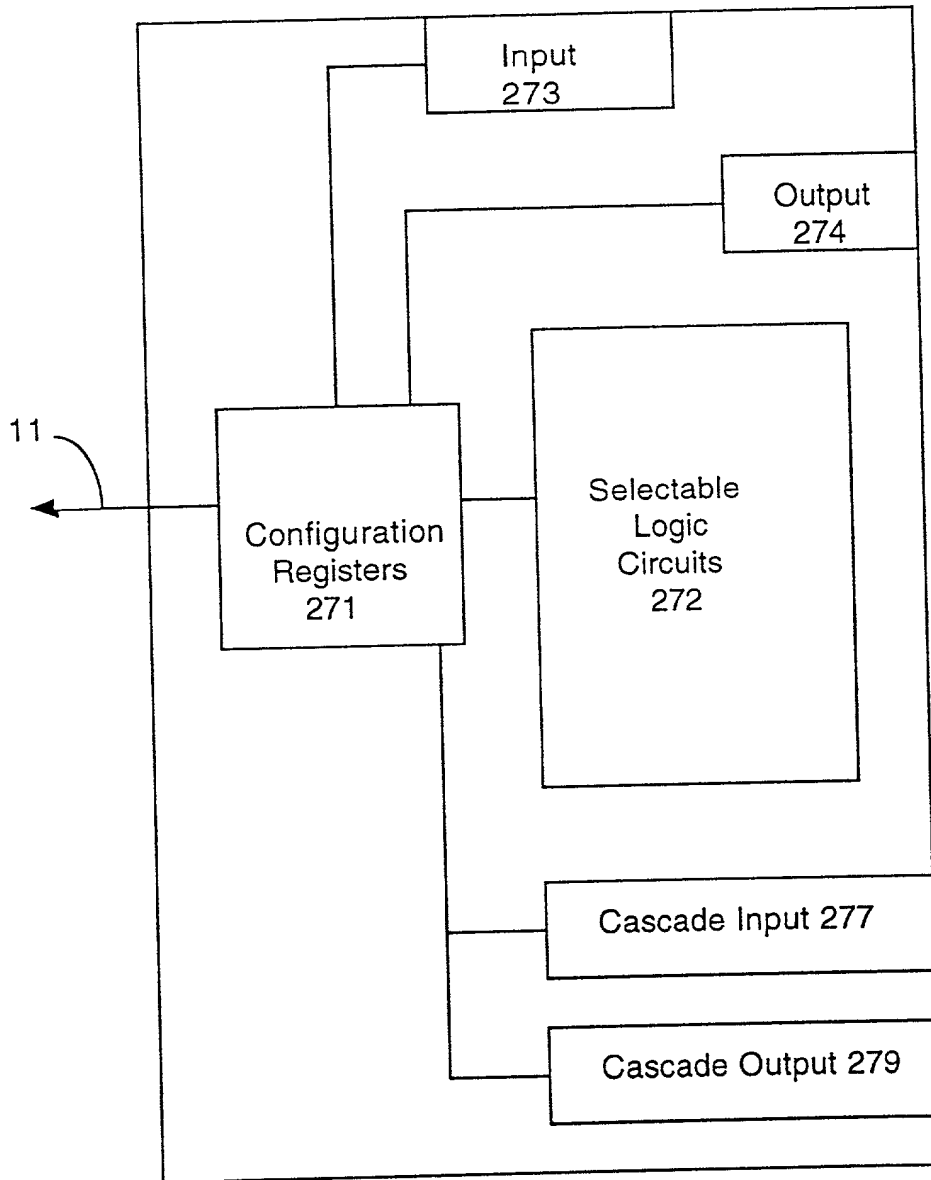


FIG 2B

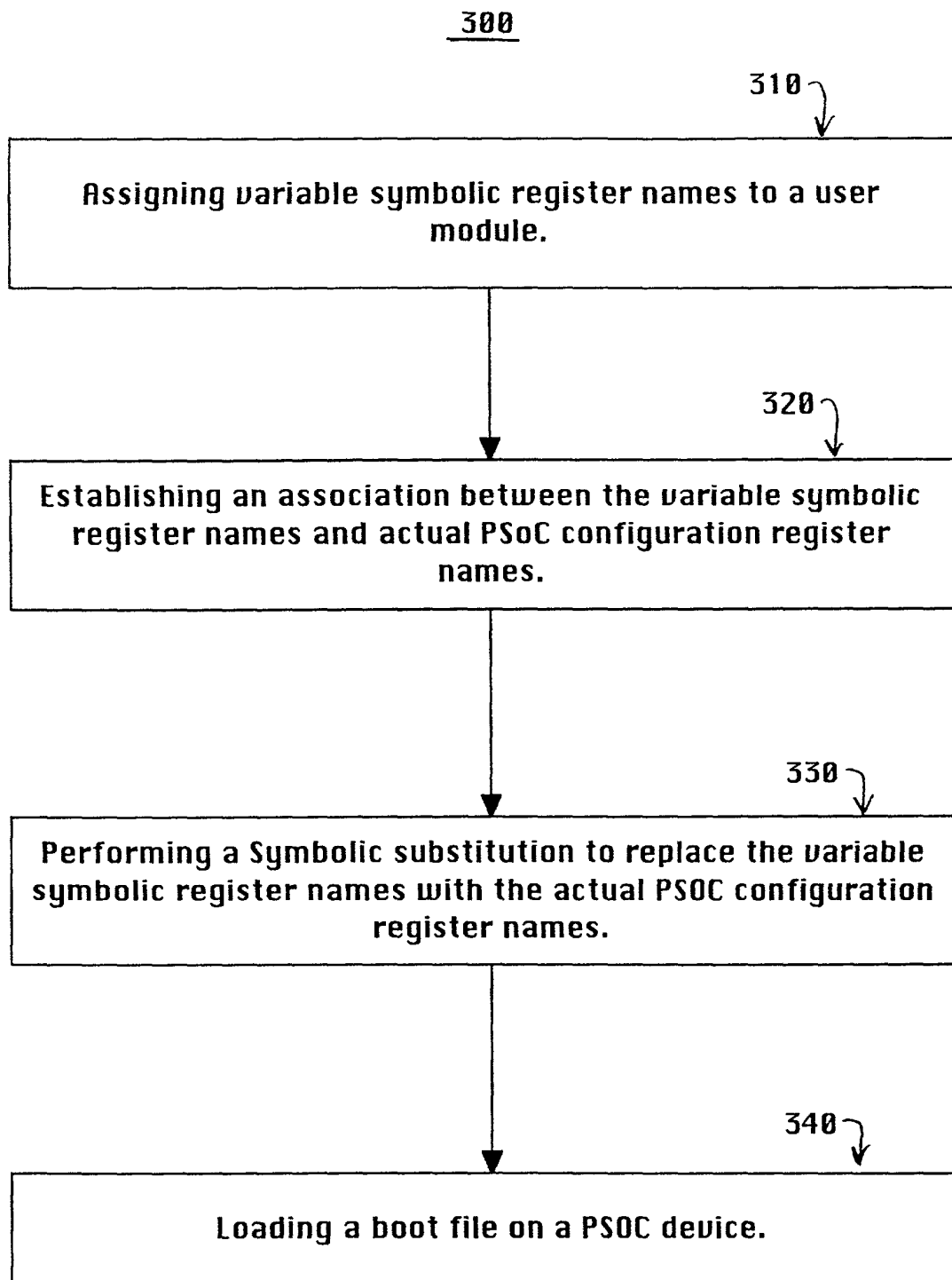


FIG 3 A

```

AREA    TOP(ROM, ABS)

org 0           ; Reset Interrupt Vector
jmp __start    ; First instruction executed following a Reset

org 04h        ; Supply Monitor Interrupt Vector
`@INTERRUPT_1`
reti

org 08h        ; PSoC Block DBA00 Interrupt Vector
`@INTERRUPT_2`
reti

org 0Ch        ; PSoC Block DBA01 Interrupt Vector
`@INTERRUPT_3`
reti

org 10h        ; PSoC Block DBA02 Interrupt Vector
`@INTERRUPT_4`
reti

org 14h        ; PSoC Block DBA03 Interrupt Vector
`@INTERRUPT_5`
reti

org 18h        ; PSoC Block DCA04 Interrupt Vector
`@INTERRUPT_6`
reti

org 1Ch        ; PSoC Block DCA05 Interrupt Vector
`@INTERRUPT_7`
reti

org 20h        ; PSoC Block DCA06 Interrupt Vector
`@INTERRUPT_8`
reti

org 24h        ; PSoC Block DCA07 Interrupt Vector
`@INTERRUPT_9`
reti

org 28h        ; Analog Column 0 Interrupt Vector
`@INTERRUPT_10`
reti

org 2Ch        ; Analog Column 1 Interrupt Vector
`@INTERRUPT_11`
reti

org 30h        ; Analog Column 2 Interrupt Vector
`@INTERRUPT_12`
reti

org 34h        ; Analog Column 3 Interrupt Vector

```

Fig 3B

400

410 ↘

Providing an interface for selecting applicable user modules.

420 ↘

Generating application files automatically

430 ↘

Facilitating programming of the desired functionality in the a target device.

440 ↘

Executing an assembler process.

450 ↘

Building a target device within the PSoC tool.

460 ↘

Emulating a target device configuration

470 ↘

Loading a configuration image generated by the PSoC Tool into memory of a PSoC target device.

FIG 4

500

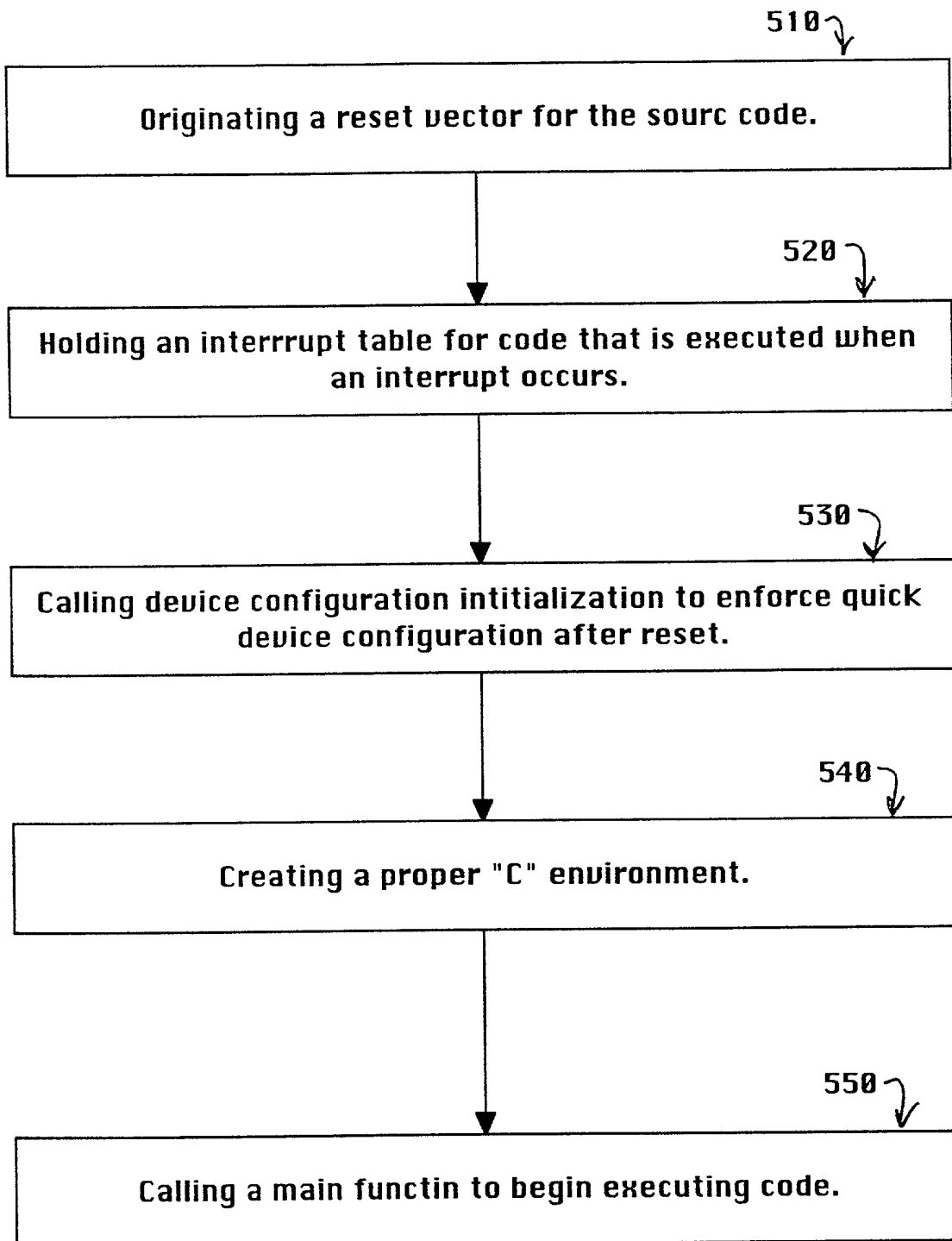


FIG 5